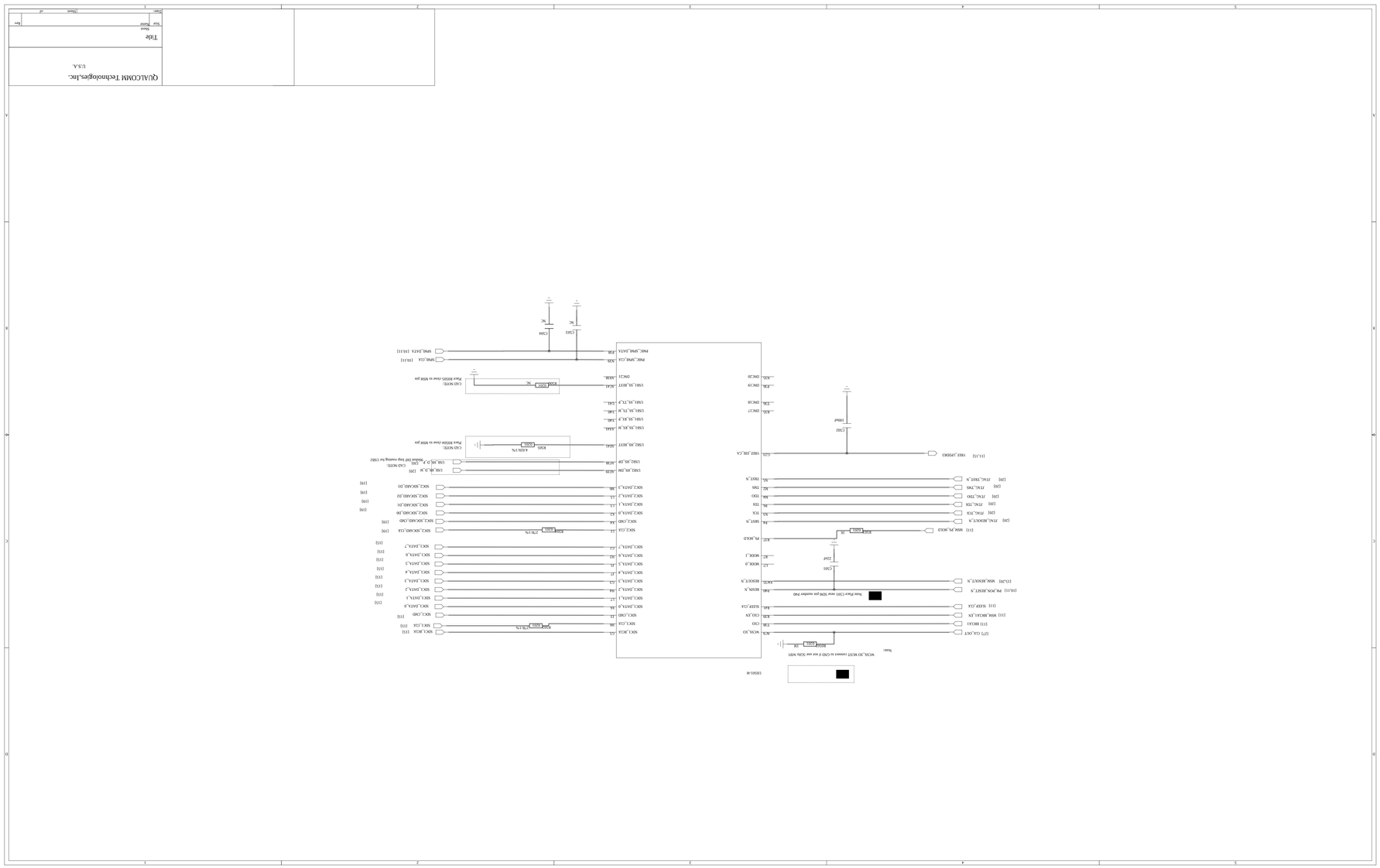
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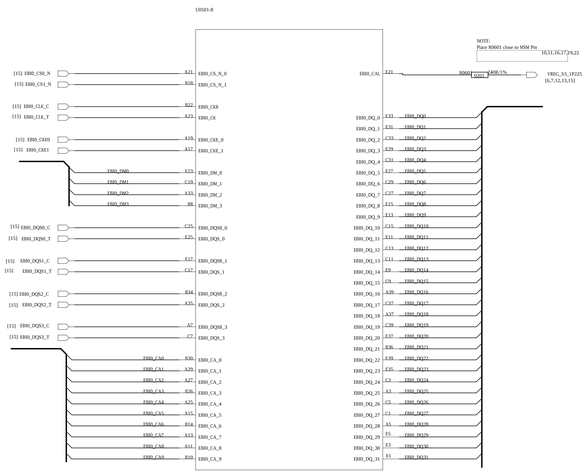

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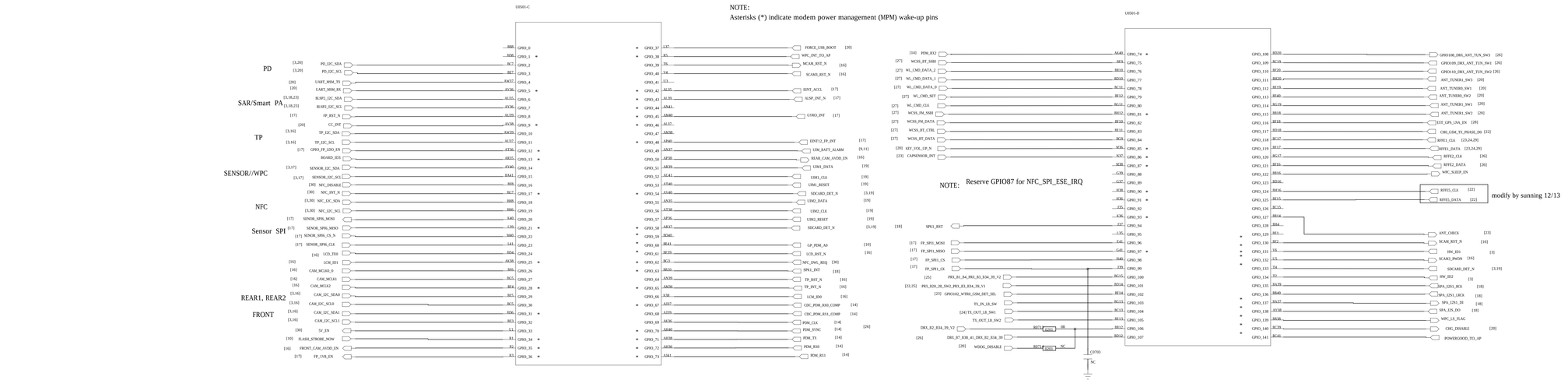
NOTE:
Asterisks (*) indicate modem power management (MPM) wake-up pins

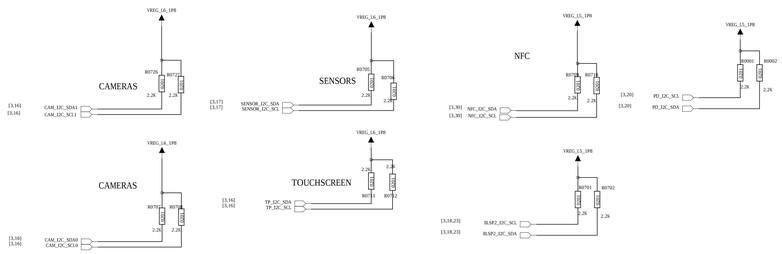


GPIO_37	FORCED_USB_BOOT
GPIO_106	WDOG_DISABLE
GPIO_108	APPS_BOOT_FROM_ROM

BOOT_CONFIG[3:1]	BOOT_CONFIG
06000	SDC1 -> SDC2 -> USB2.0
06001	SDC2 -> SDC1-> USB2.0
06010	SDC1-> USB2.0
06011	USB2.0

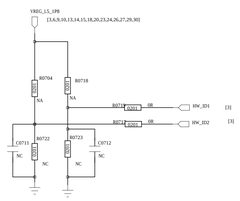
Default Boot Config (06000) is SDC1(eMMC)

I2C PULL-UP RESISTORS



NOTE:
Ensure SW sets these GPIOs (Sensor, CTP and Camera I2C bus) to inout pull down when the peripherals are powered off to eliminate leakage.

MSM8953 GPIO



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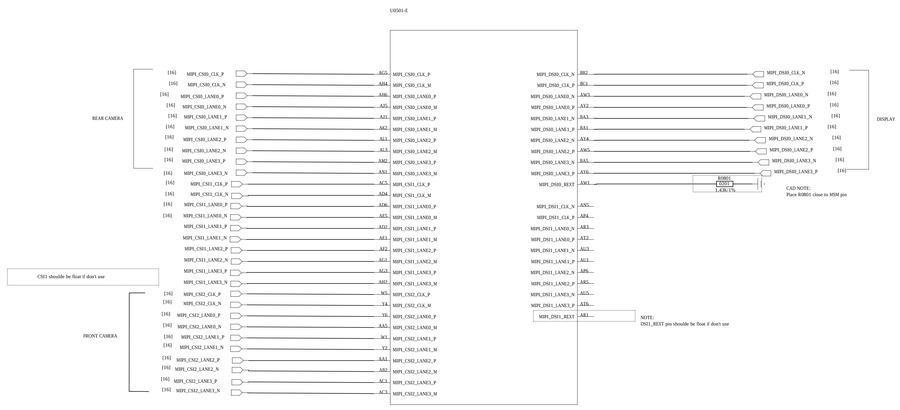
REV:



CSI2_3PHASE_PIN9 CSI2_DN3

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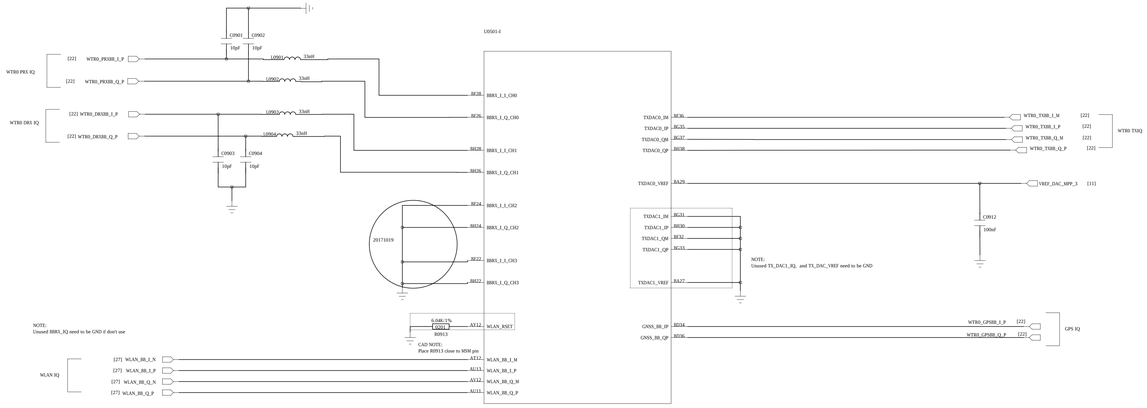
Date | Sheet of



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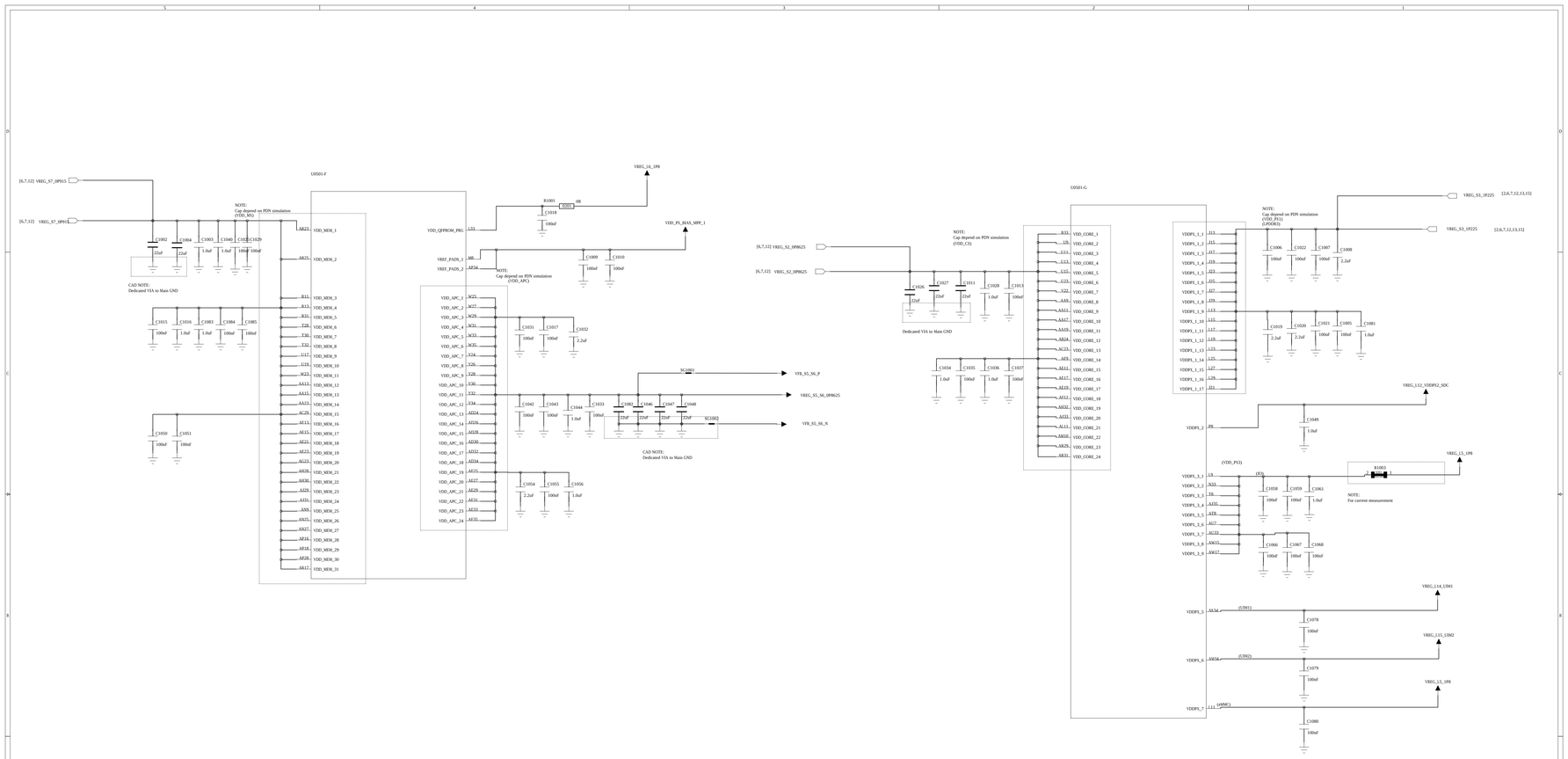

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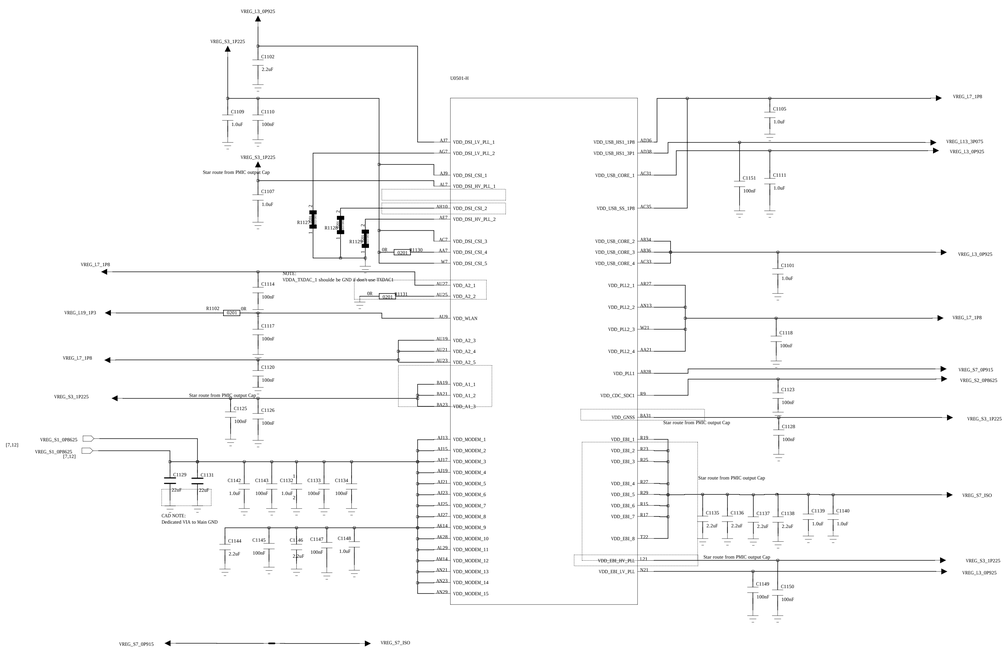
CAD NOTE:
About star route from PMIC output Cap:



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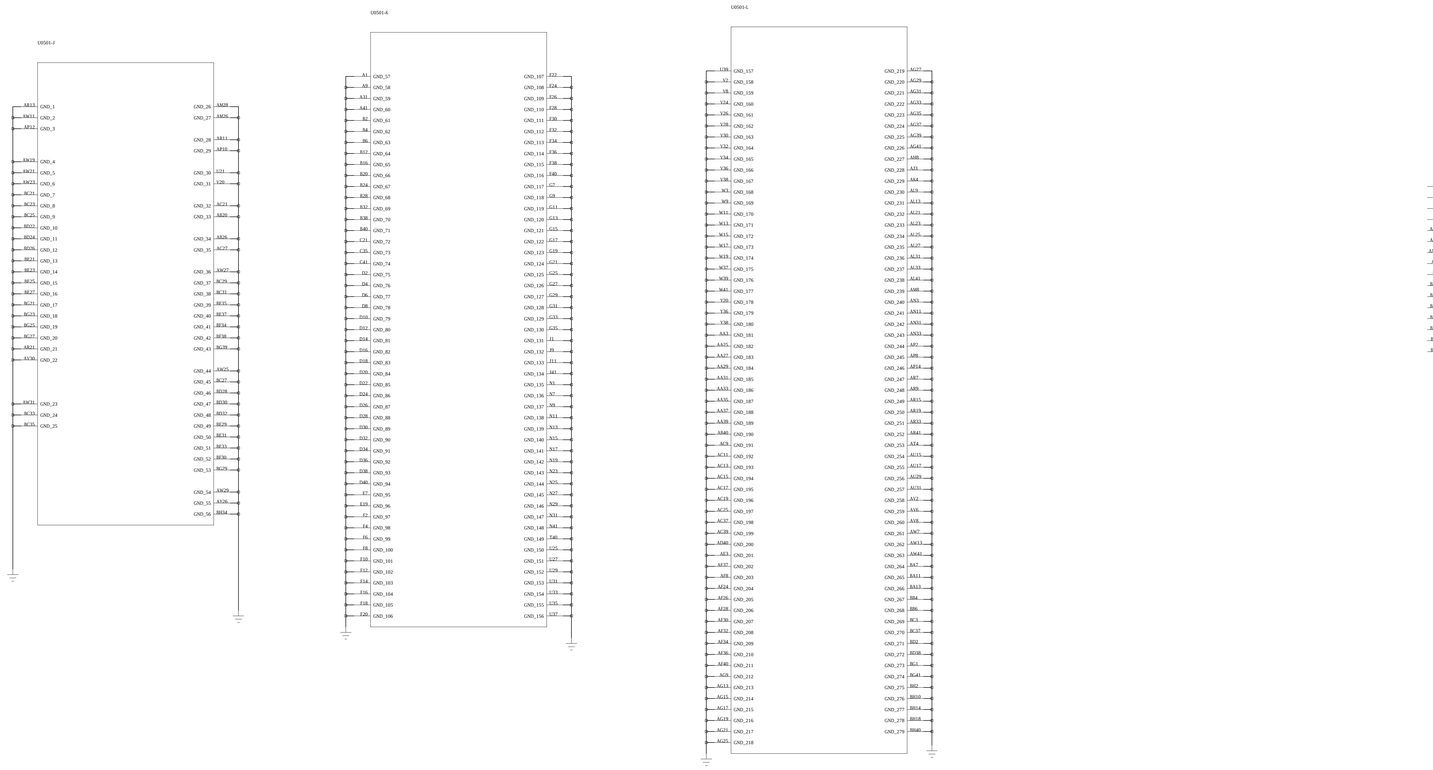

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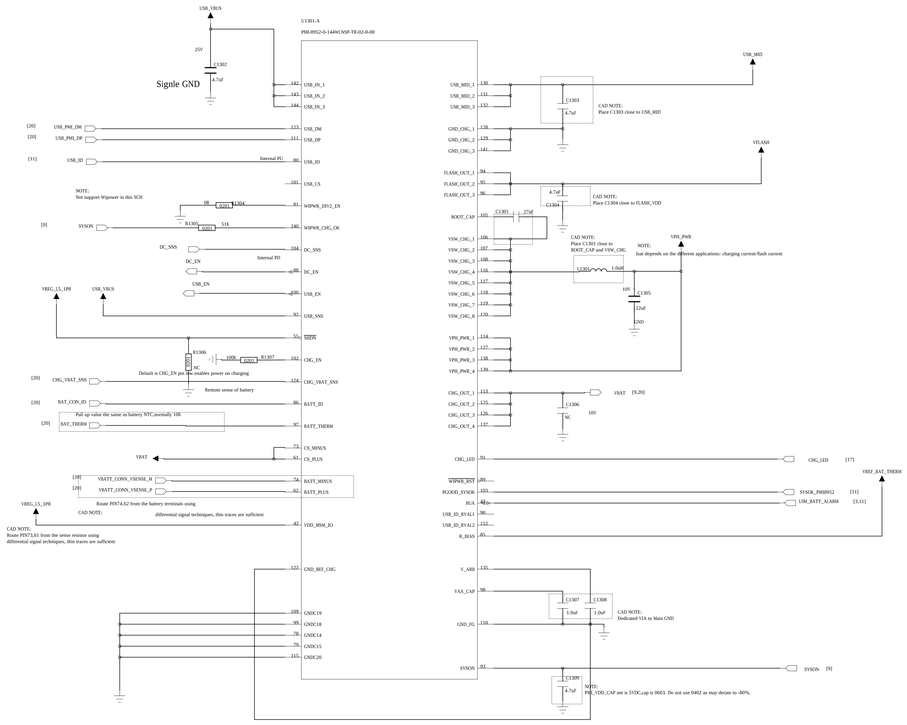
Date | Sheet of

PMI PIN	connection on device without W/power	connection on device with W/power
CHG_OK	Pull up to VDD_CAP with 51kohm	Stark PRU CHG_OK
DVI2_EN	Pull down to GND with 8ohm	Stark PRU DVI2_EN
GPIO2	No Connect	10kohm pull down to gnd
	For parallel charging: Connect to SMB EN pin. Do NOT pull up to VDD_CAP	For parallel charging: Connect to SMB EN pin. Do NOT pull up to VDD_CAP

Use C1302= 4.7uF for Single Charging Config
Use C1302 = 2.2uF for Parallel Charging Config



PMI8952 Charger

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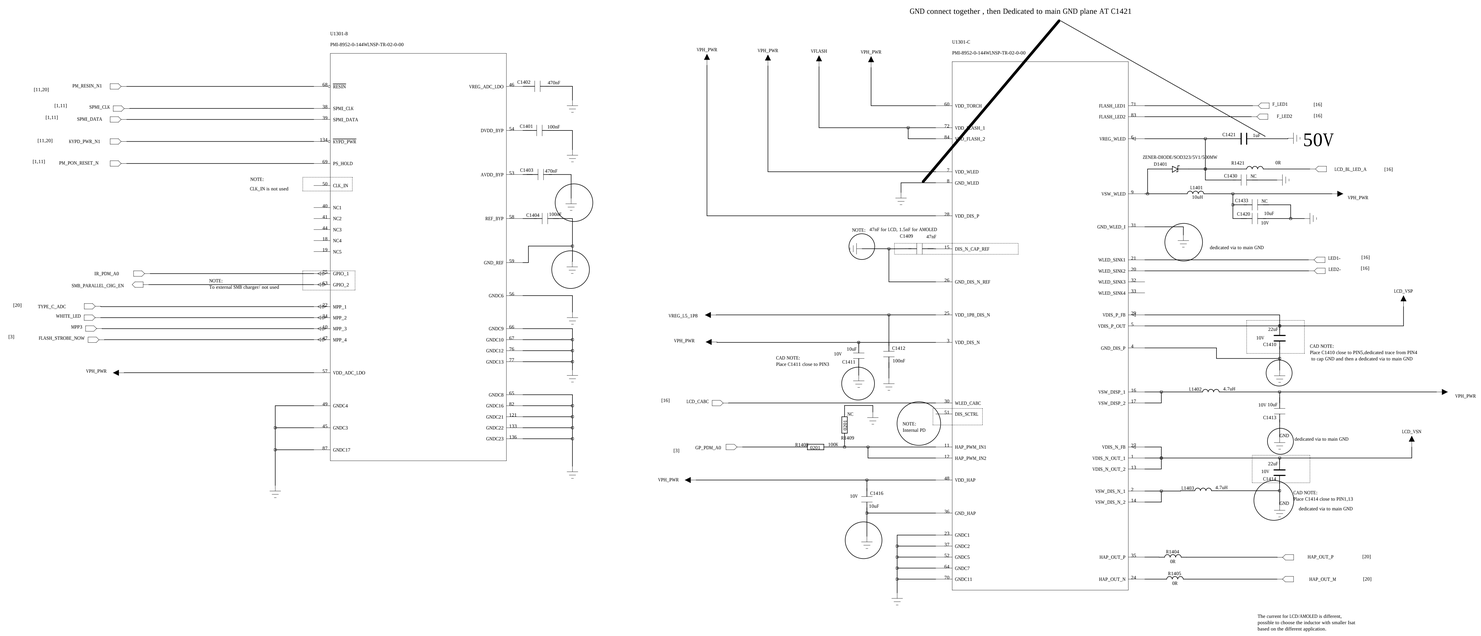

PMI8952 Control/Interface

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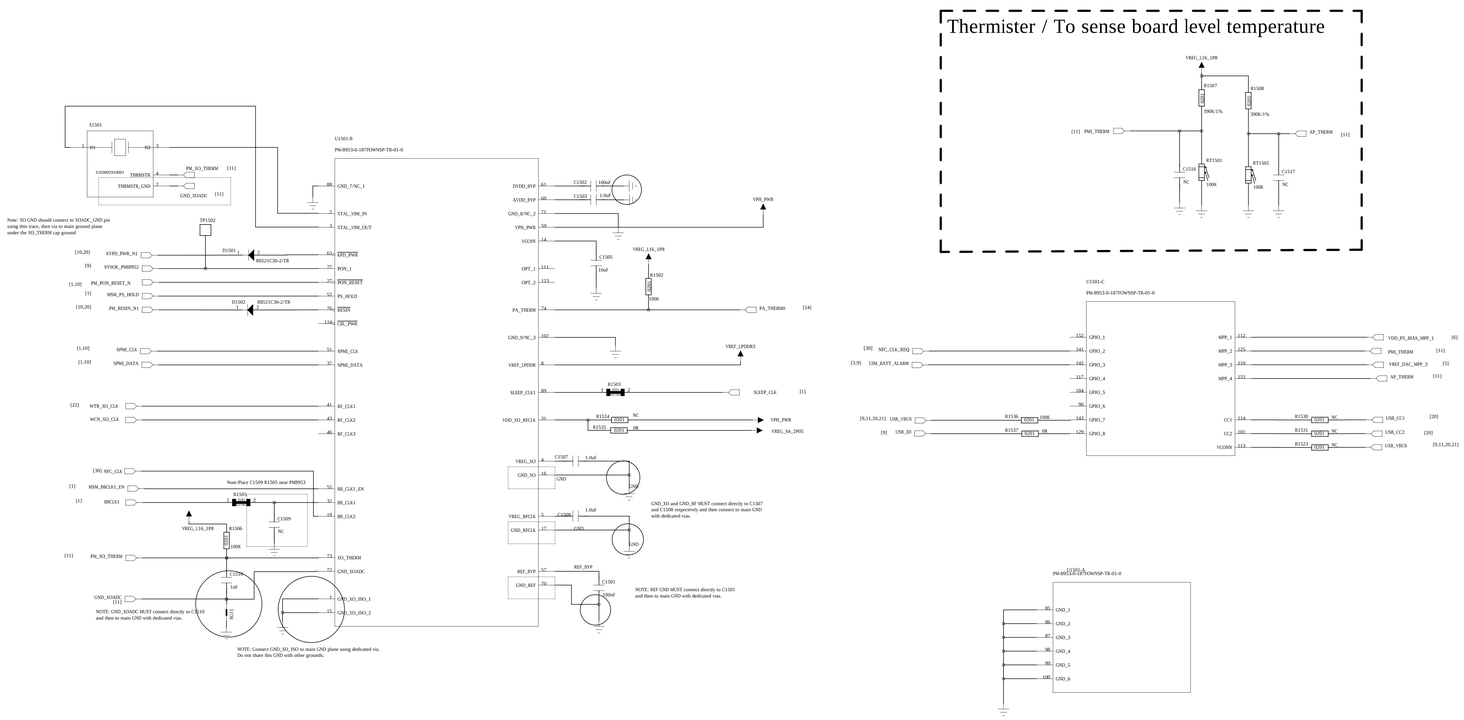

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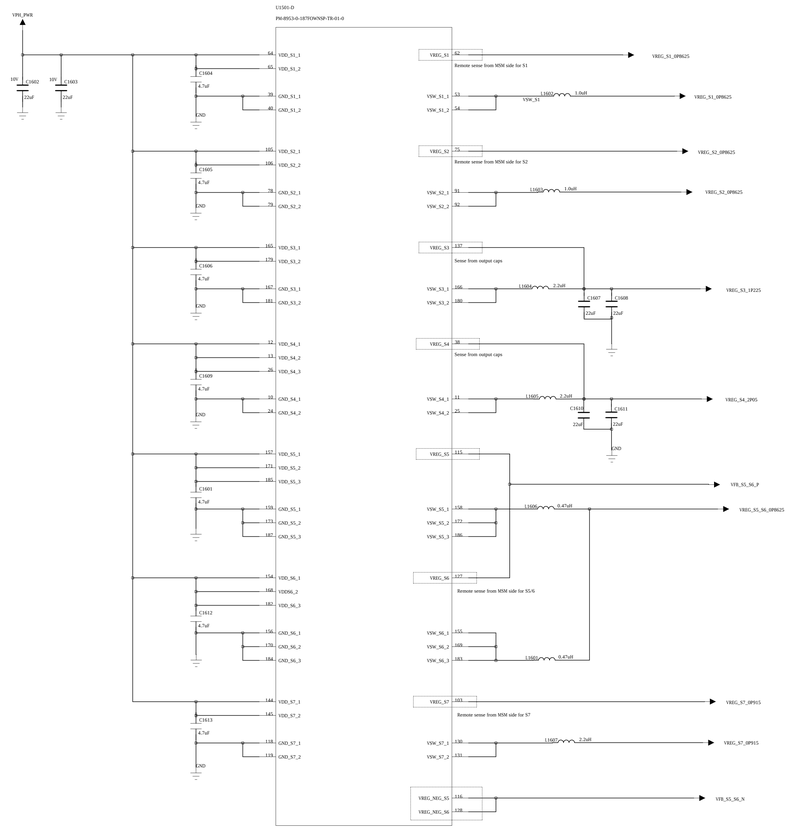
Title

Size | Name | Rev

Date | Sheet | of



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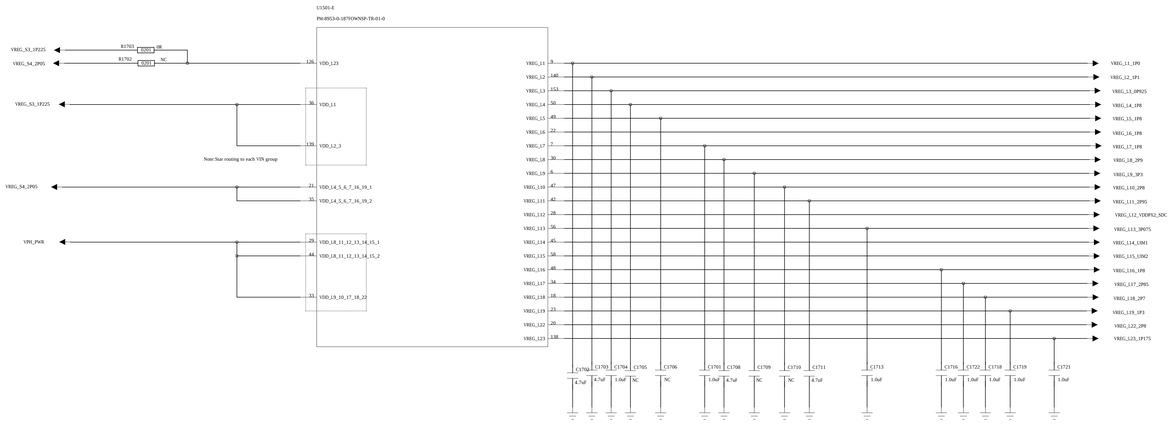
Title

Size | Sheet Name | Rev

Date: | Sheet of



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Title

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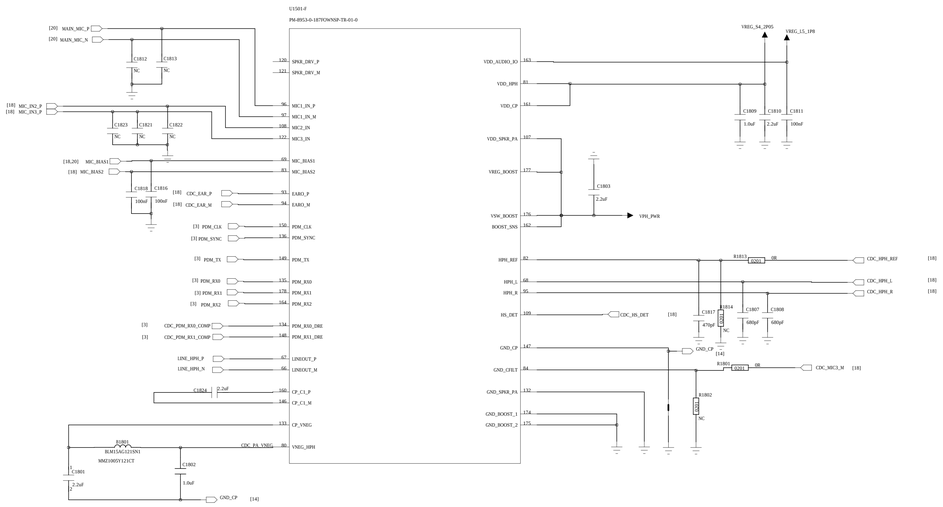
Date: | Sheet of



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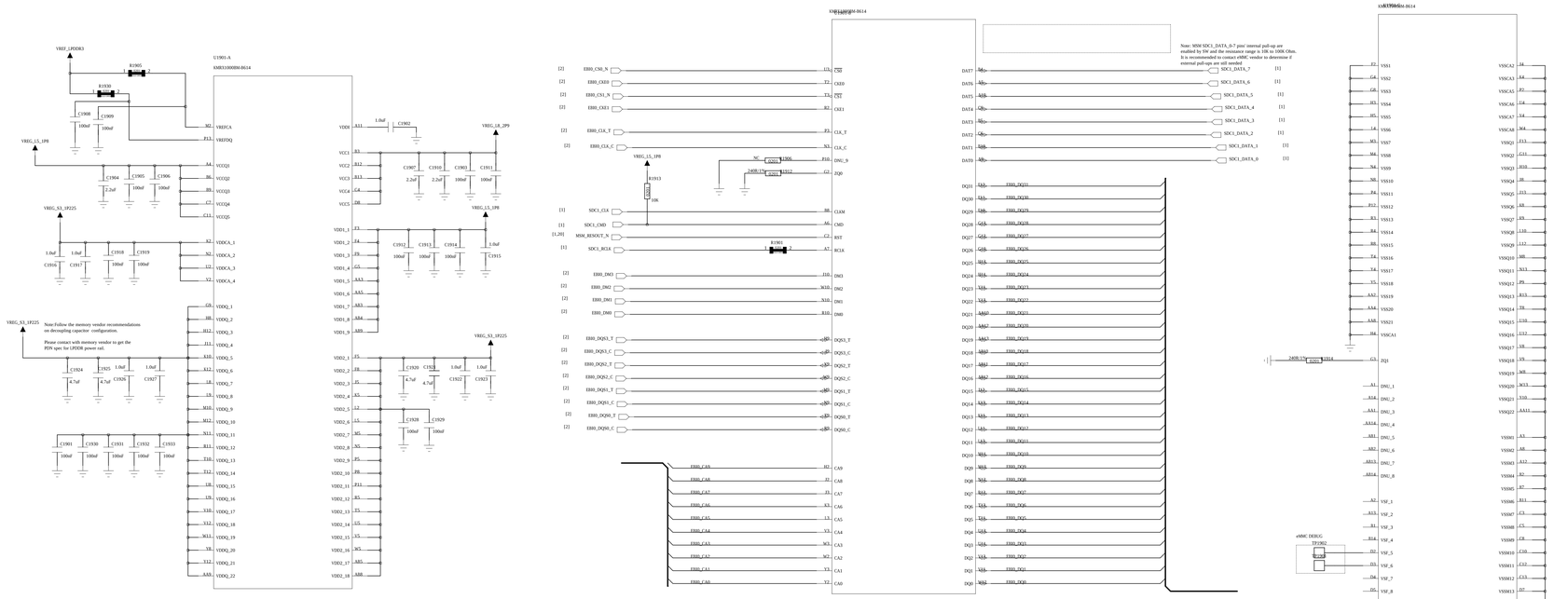
Date: Sheet of



MEMORY(LPDDR3+EMMC V5.1)

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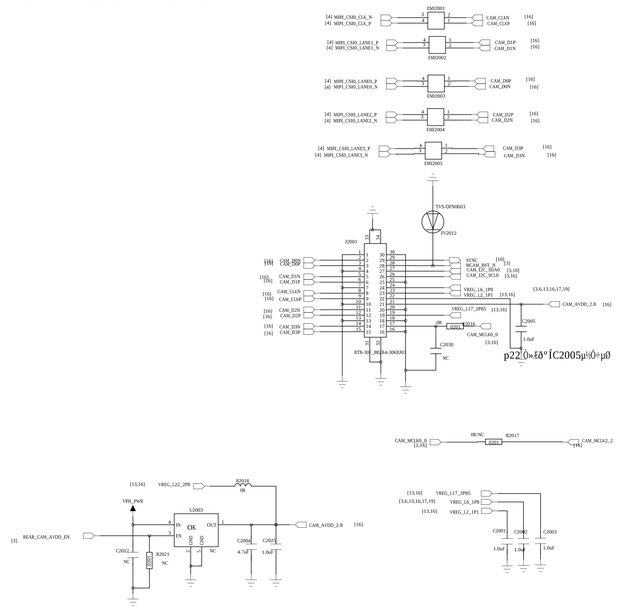
Title

Size | Sheet Name | Rev

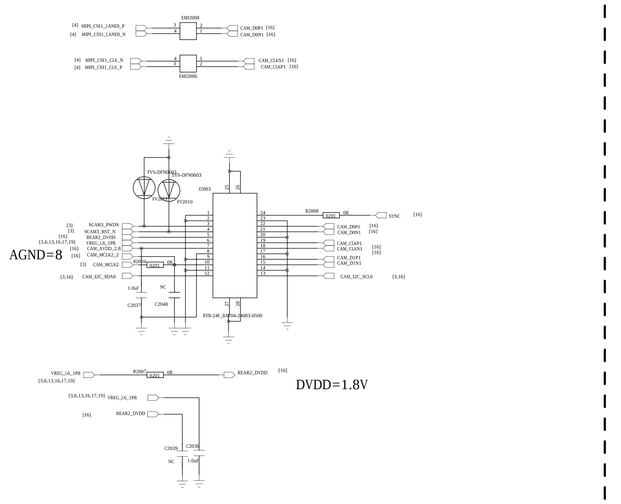
Date: | Sheet of

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

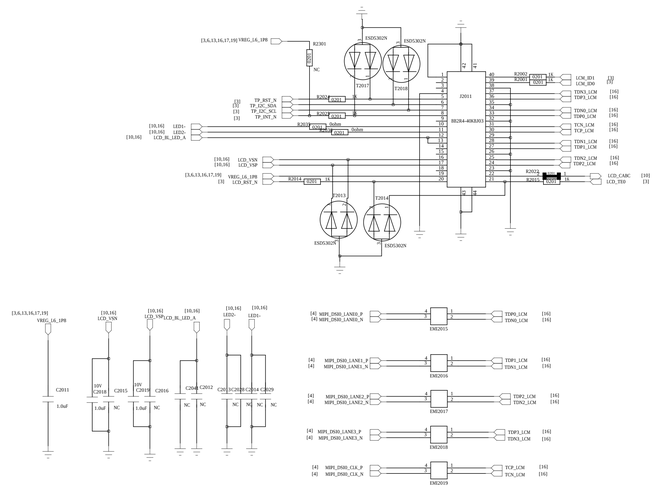
Main Camera A



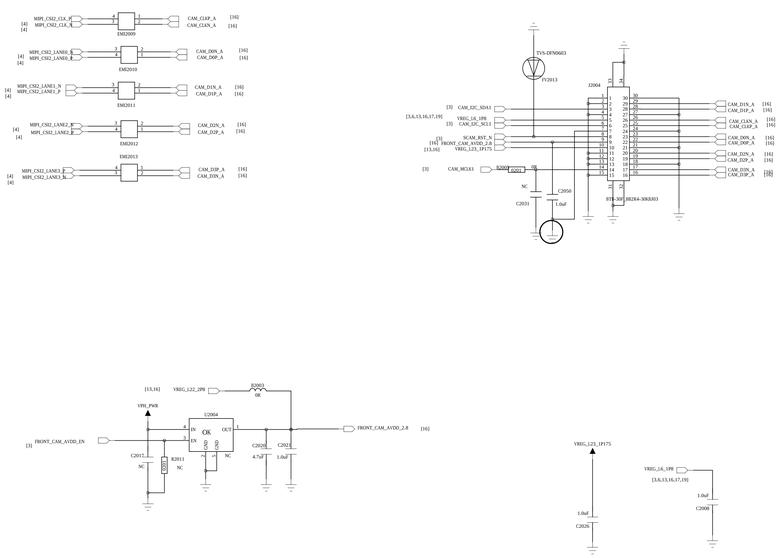
Main Camera B



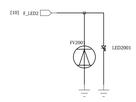
LCM



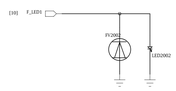
Front Camera



Front FLASHLIGHT



Main FLASHLIGHT



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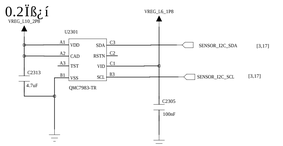
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

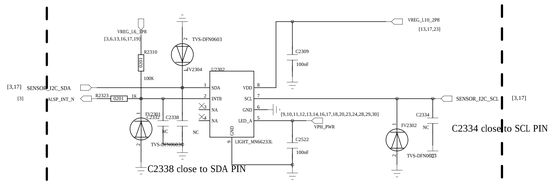
M-Sensor

I2C address :110000(30)

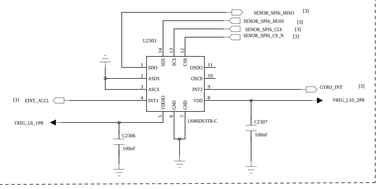


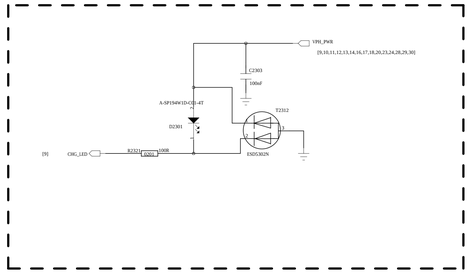
P_SENSOR

0x92

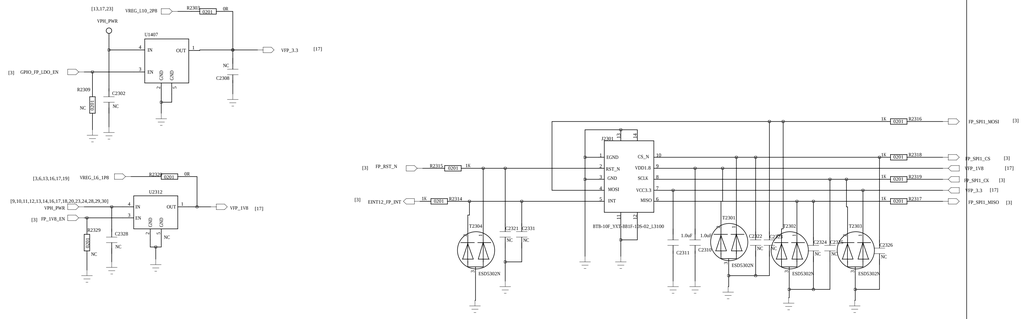


A+Gyro-Sensor



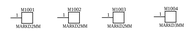


FingerPrint-Sensor



[3,6,13,16,17,19] VREG_1.6_1P8

MARK POINT



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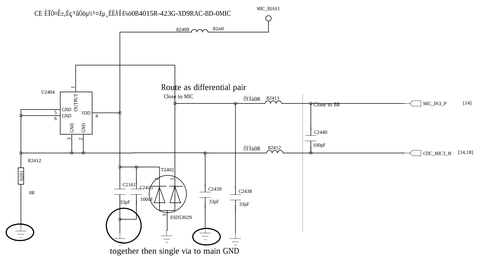
Sub MIC



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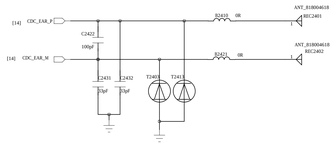
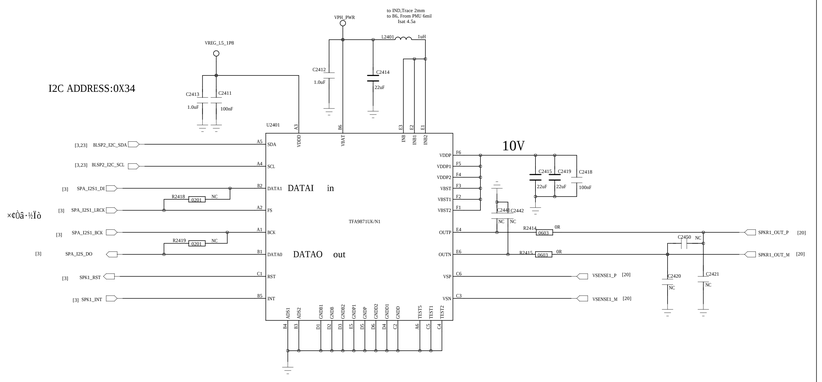
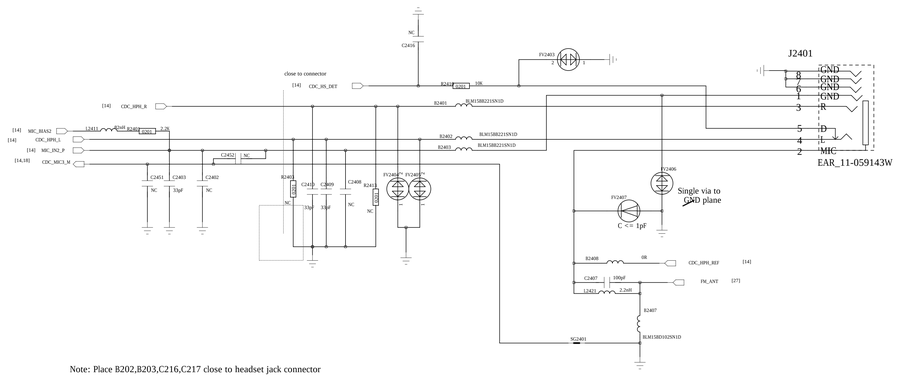


Audio PA



Earphone Audio

Note: Ferrite beads and their corresponding bypass capacitors on on CDC_HPH_L_P, CDC_HPH_L_M and CDC_HPH_REF are needed to reduce noise generated by audio/FM concurrency



Note: Place B202,B203,C216,C217 close to headset jack connector

Note: For Cable detection

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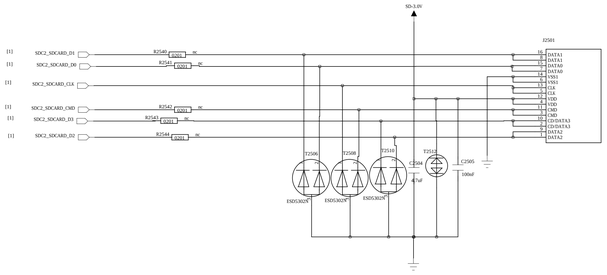
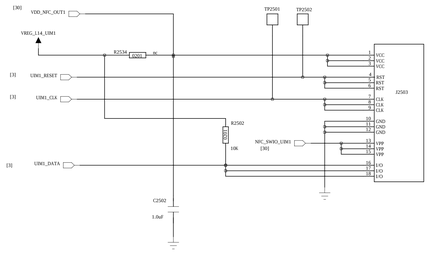
LTR	ECO NO:	APPROVED:	DATE:

T-Card



SIM1



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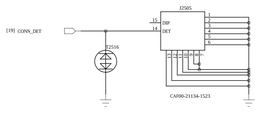
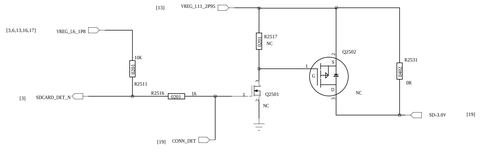


TF SWITCH



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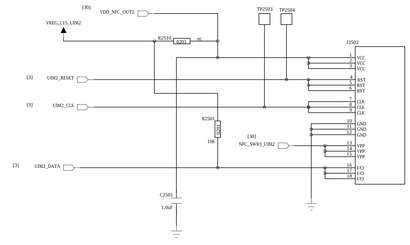

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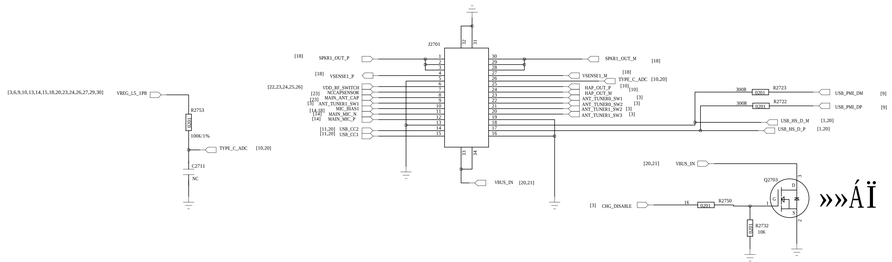
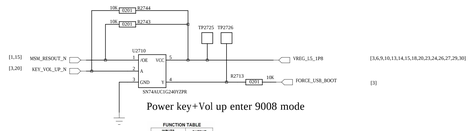
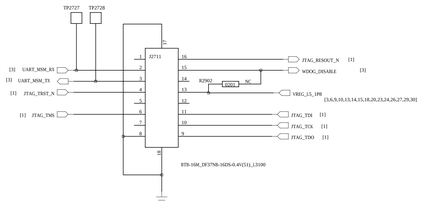
SUB_FPC



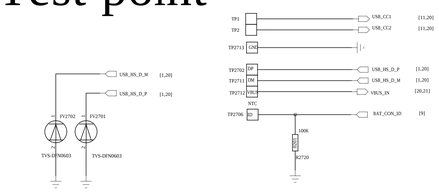


Power key+Vol up enter 9008 mode

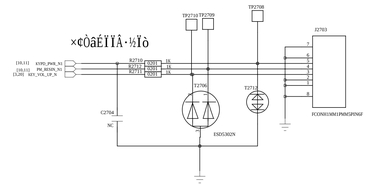
FUNCTION TABLE		
INPUTS		OUTPUT
OE	A	Y
L	L	L
L	H	H
H	X	Z



Test point



Power Key

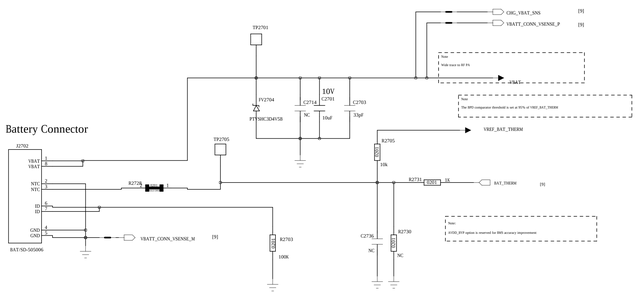


Schematic design notice of "65_PERI_Dual_SIM_ICUSB_KEYPAD" page.

Note 65-1: DO NOT put pull-up resistor on PWRKEY

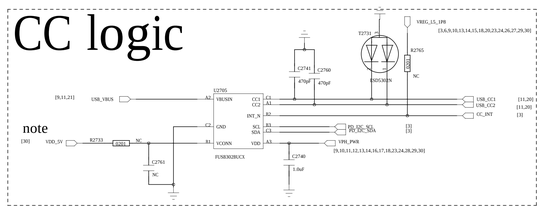
Note 65-2: Volume Up : HOME Key / GND
Volume Down : (KPROW0/KPCOL0) or KPCOL0 / GND

BAT

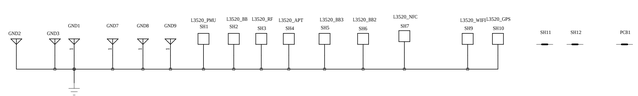


Note:
Battery ID resistor value require 20K~150K,
Add 100K to ground If battery package ID not meet requirement

CC logic



note



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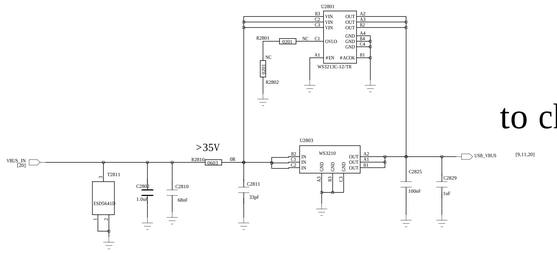
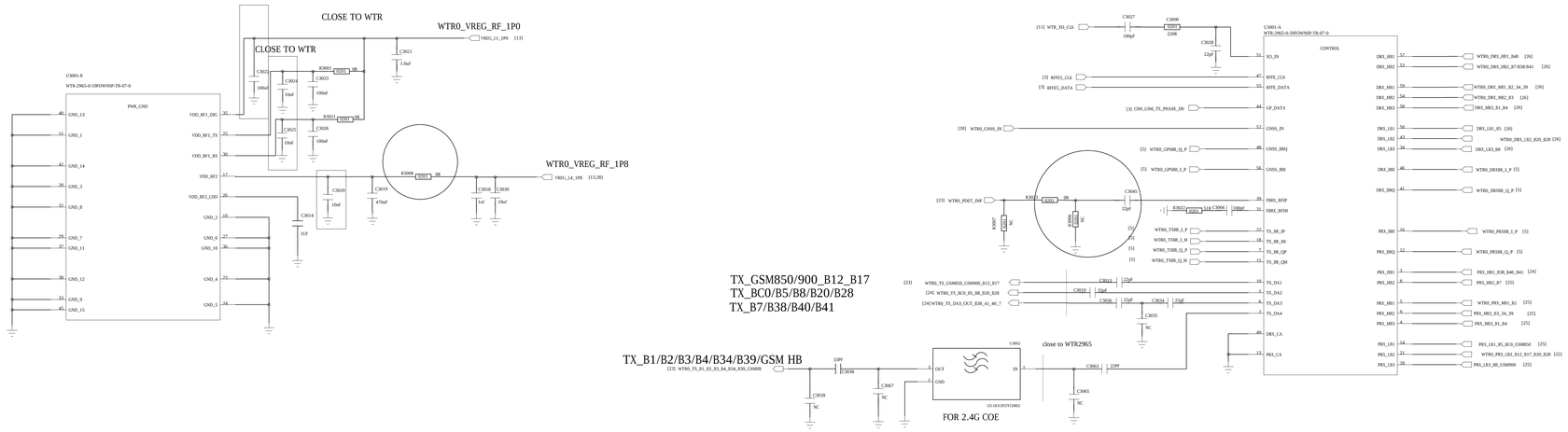
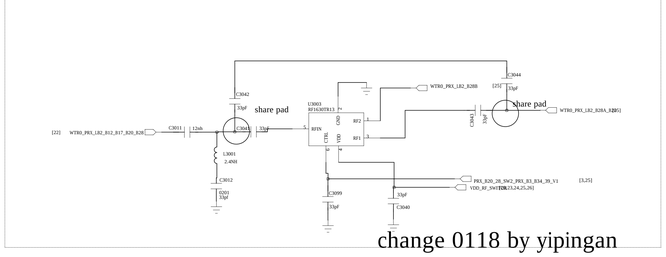


to charger IC

DRAWN: <Drawn By>	DATED: <Drawn Date>	COMPANY: <Company Name>	
CHECKED: <Checked By>	DATED: <Checked Date>	TITLE: <Title>	
QUALITY CONTROL: <QC By>	DATED: <QC Date>	CODE: <Code>	SIZE: A0 / DRAWING NO: <Drawing Number> / REV: <Revision>
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 24 OF 30

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:





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SCALE: <Scale>		SHEET: 22 30	

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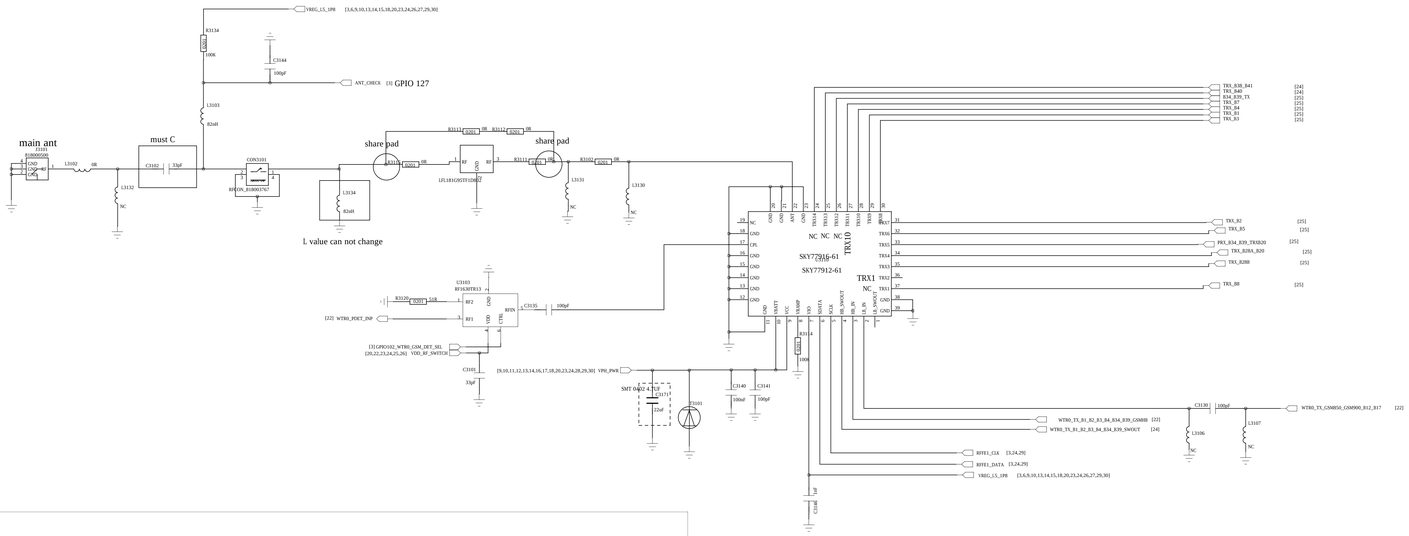
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



SAR Sensor

REVISION RECORD			
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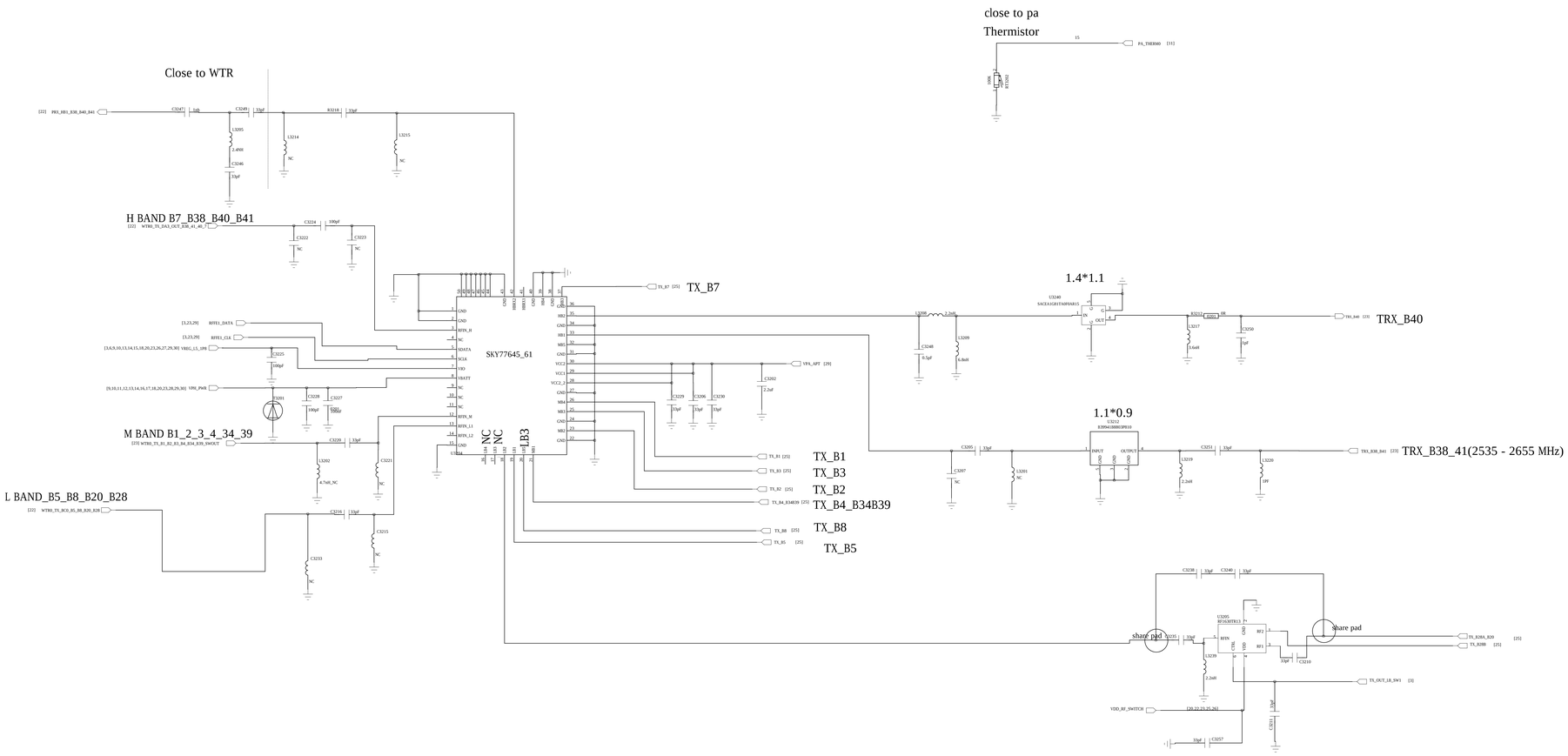

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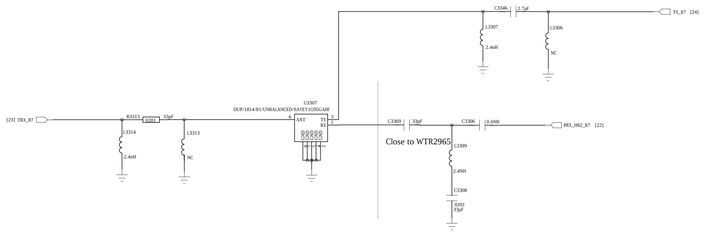
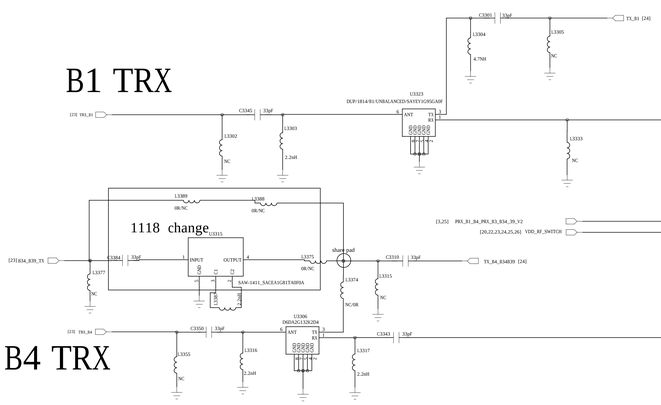
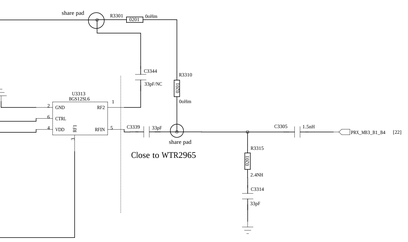
B7 TRX



B1 TRX

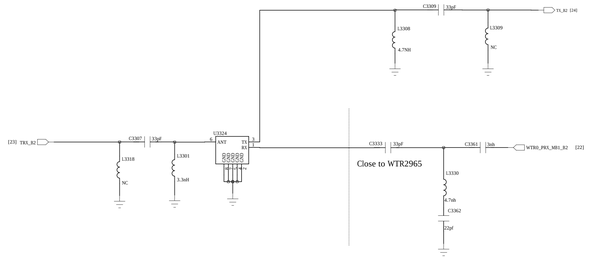


SPDT SWITCH

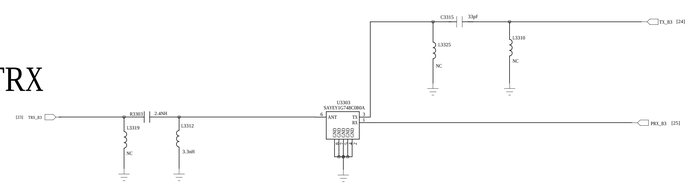


B4 TRX

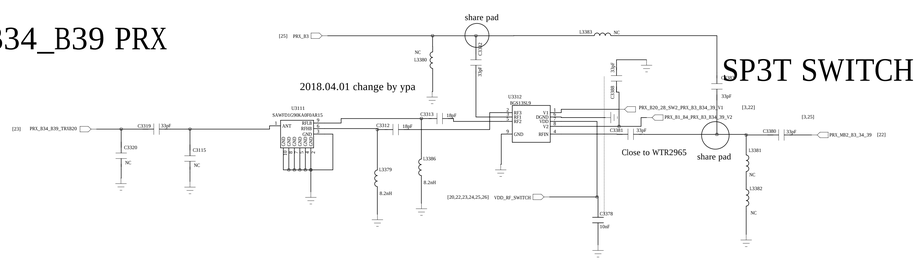
B2 TRX



B3 TRX

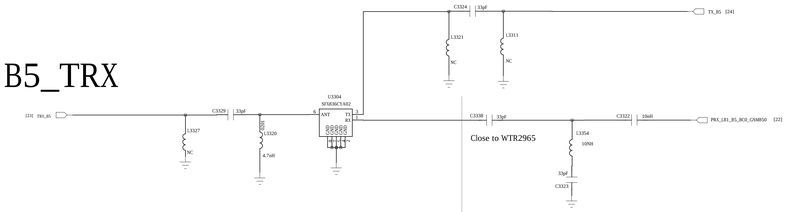


B3_B34_B39 PRX

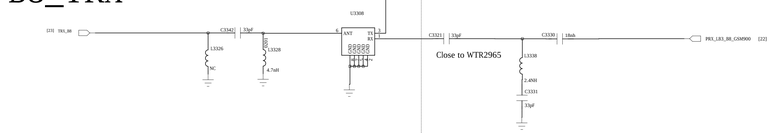


SP3T SWITCH

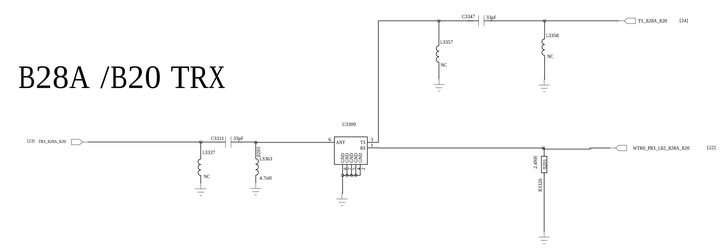
B5_TRX



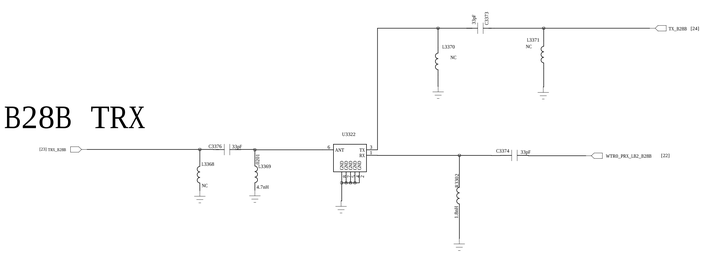
B8_TRX

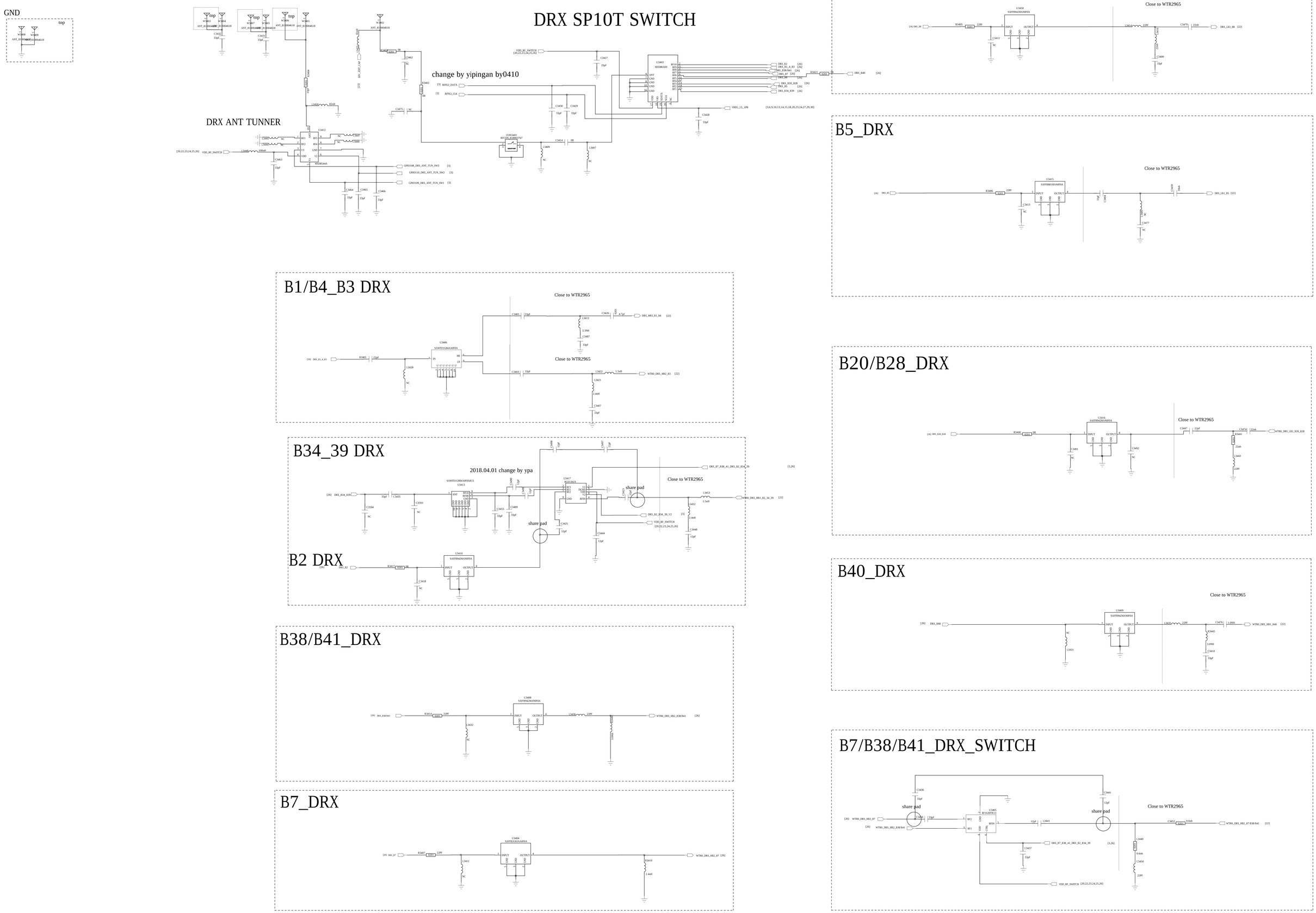


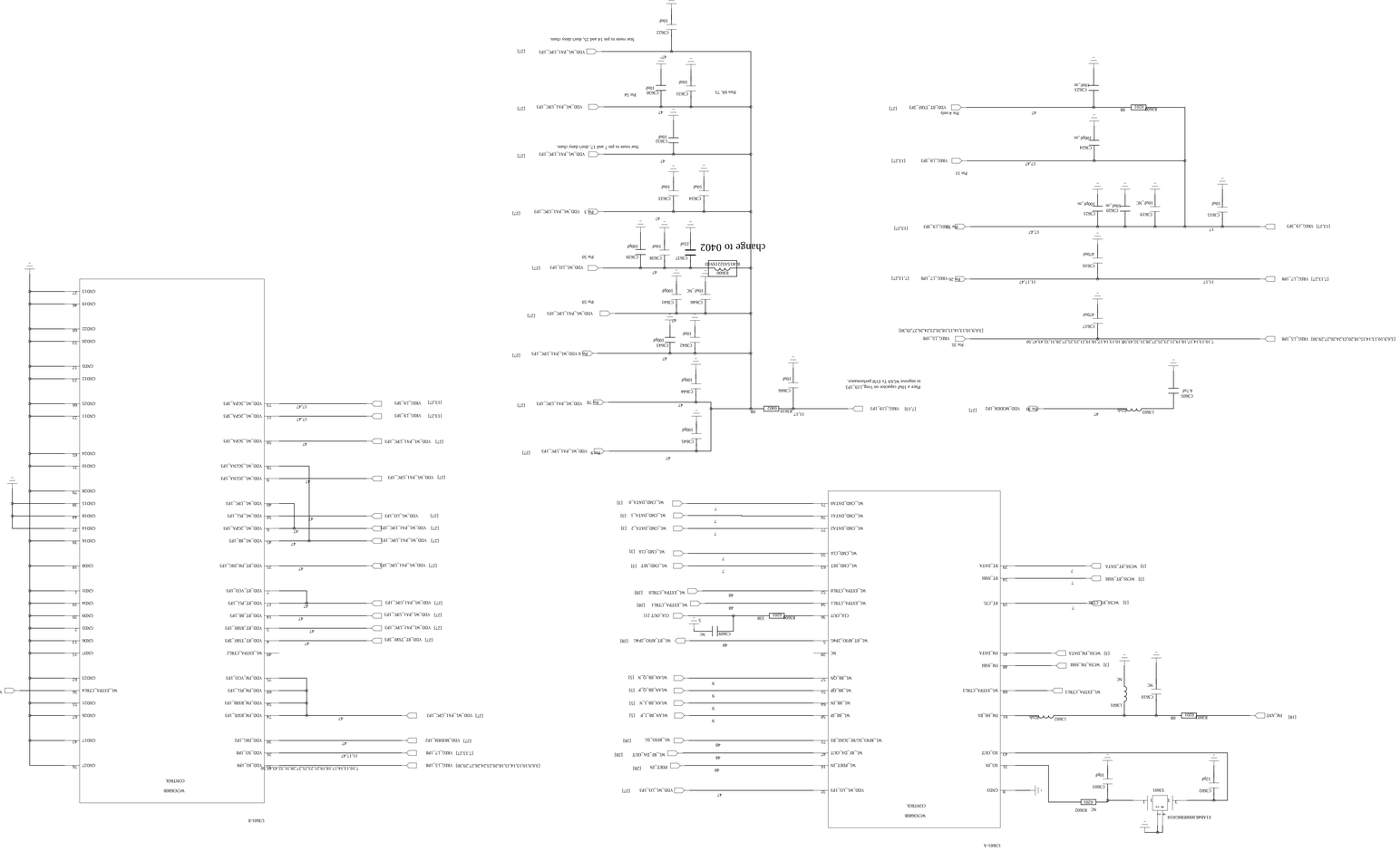
B28A /B20 TRX



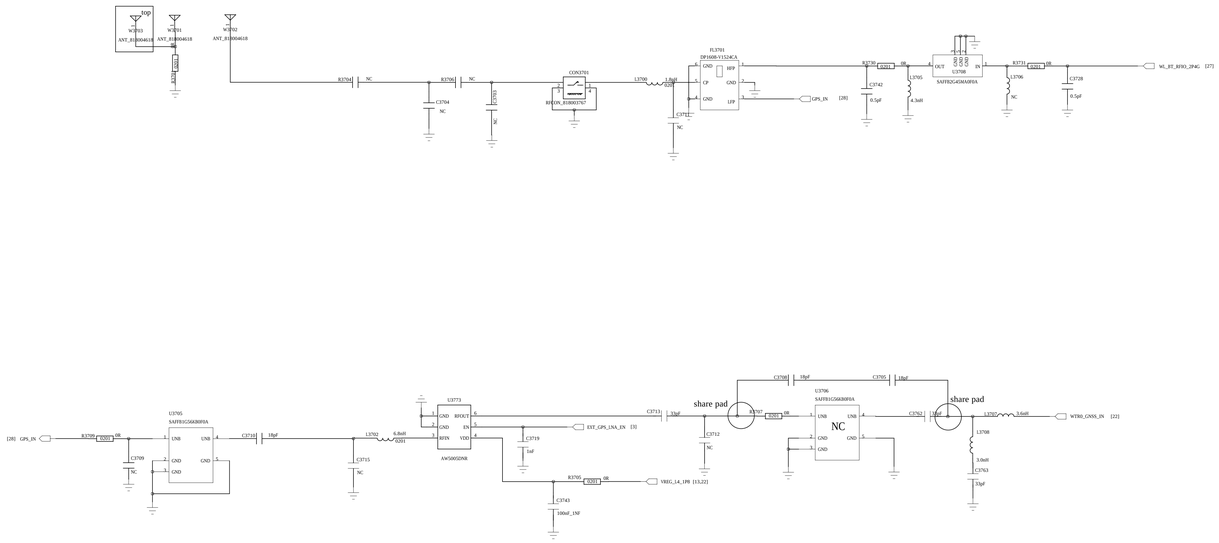
B28B TRX



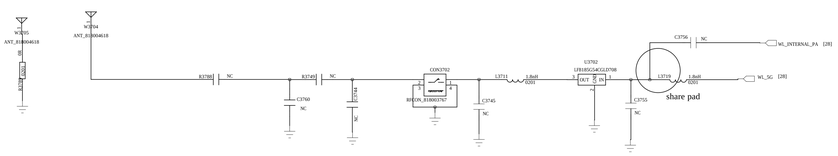

GND
top
DRX SP10T SWITCH
change by yipingan by0410
DRX ANT TUNNER
B8_DRX
Close to WTR2965
B5_DRX
B1/B4_B3 DRX
B20/B28_DRX
B34_39 DRX
2018.04.01 change by ypa
share pad
B2 DRX
B38/B41_DRX
B40_DRX
B7_DRX
B7/B38/B41_DRX_SWITCH

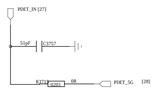

change to 0402
LS601-A
LS601-B
WCS6500
CONTROL

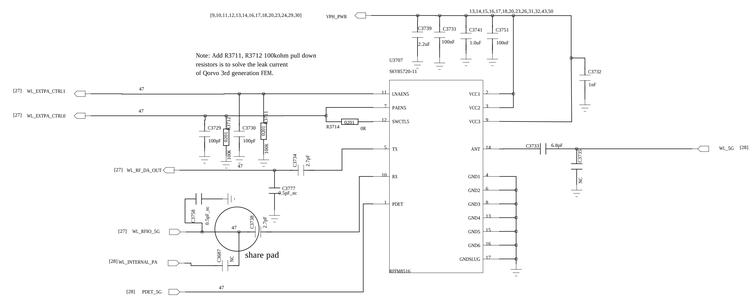
GPS WIFI 2.4G Ant



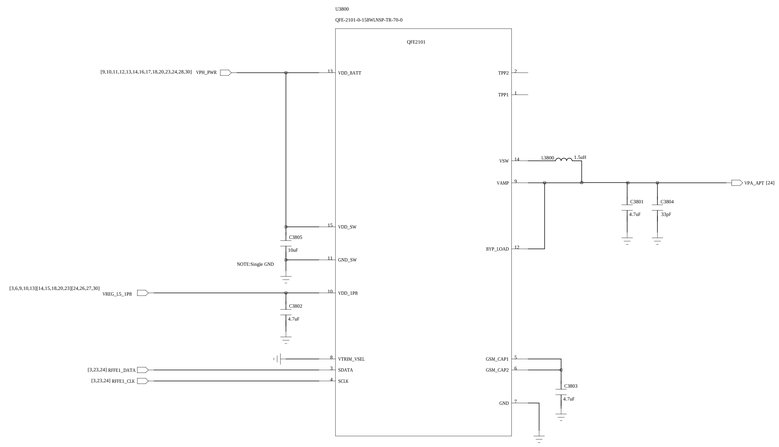
WIFI 5G Ant



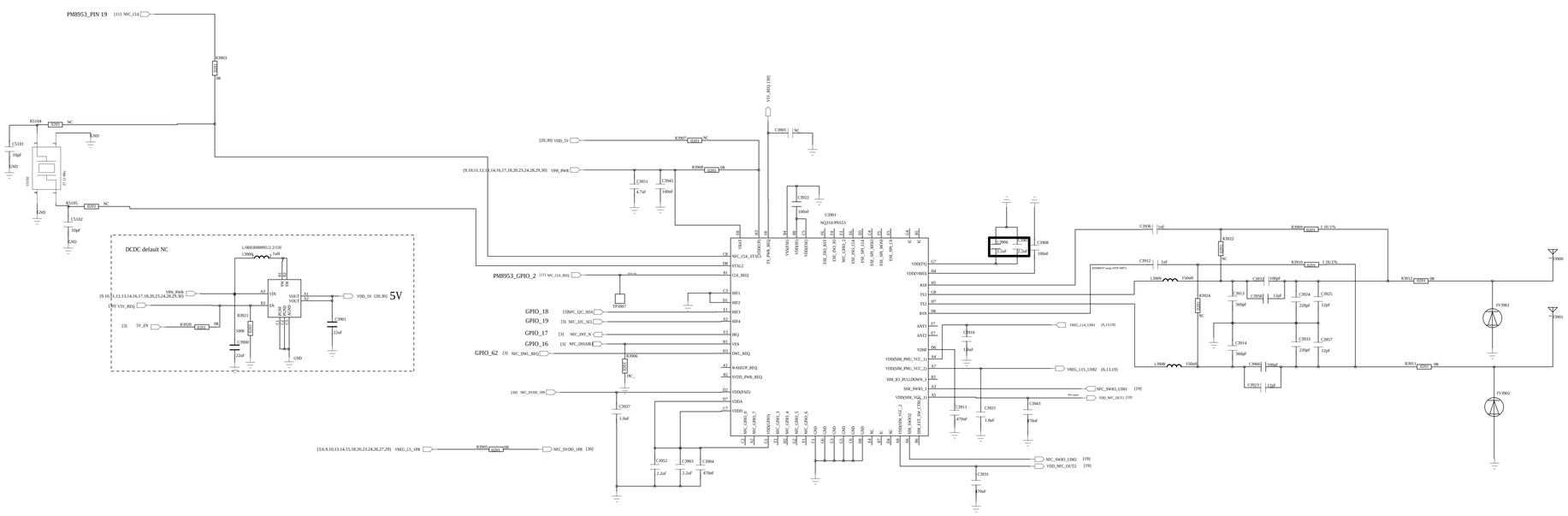




APT


U3800
QFE-2101-0-15dW/NSP-TR-70-0
QFE2101
[9,10,11,12,13,14,16,17,18,20,23,24,28,30]
VPH_PWR
13
VDD_BATT
15
VDD_SW
C3805
10nF
NOTE:Single GND
11
GND_SW
[3,6,9,10,13]14,15,18,20,23]24,26,27,30]
VREG_L5_1PR
10
VDD_1PR
C3802
4.7uF
1
8
VTRM_VSEL
[3,23,24]
RFFE1_DATA
3
SDATA
[3,23,24]
RFFE1_CLK
4
SCLK
TPP2
2
TPP1
1
VSW
14
L3800
2.5nH
VAMP
9
VPA_APT [24]
C3801
4.7uF
C3804
33pF
BYP_LOAD
12
GSM_CAP1
5
GSM_CAP2
6
C3803
4.7uF
GND
7

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: <Drawn By>	DATED: <Drawn Date>	COMPANY: <Company Name>	
CHECKED: <Checked By>	DATED: <Checked Date>	TITLE: <Title>	
QUALITY CONTROL: <QC By>	DATED: <QC Date>	CODE: <Code>	SIZE: A0
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